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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,812	11/19/2001	Craig Nemecek	CYPR-CD01219M	1802

7590 06/02/2005

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EXAMINER

CRAIG, DWIN M

ART UNIT	PAPER NUMBER
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2123

DATE MAILED: 06/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/989,812

Applicant(s)

NEMECEK ET AL.

Examiner

Dwin M Craig

Art Unit

2123

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 November 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,3,4,6-8,10,11,13-15 and 20 is/are rejected.
- 7) ☒ Claim(s) 2, 5, 9, 12, 16-19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 November 2001 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. **Claims 1-20** have been presented for Examination.

Drawings

2. The drawings submitted with this Application are objected to. This application has been filed with informal drawings which are acceptable for examination purposes only, specifically Figures 2 and 3 have minor informalities which require correction.

Specification

3. The abstract of the disclosure is objected to because the number of words exceeds 150. Correction is required. See MPEP § 608.01(b), and ...

6.02 Content of Specification

(j) Abstract of the Disclosure: A brief narrative of the disclosure as a whole in a single paragraph of 150 words or less commencing on a separate sheet following the claims.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. **Claims 1, 3, 4, 6, 7, 8, 10, 11, 13, 14, 15 and 20** are rejected under 35 U.S.C. 102(a) as being anticipated by **Coomes U.S. Patent 6,499,122**.

4.1 As regards Independent **Claims 1, 8 and 15** and using Independent **Claim 1** as an example, the *Coomes* reference teaches, “a power supply over-driver protection system for a DUT” (**Figure 2 item 201** shows the master control block which controls the over-drive protection system **Col. 6 lines 22-30**), “a processor coupled to a memory via a bus having

instructions that when executed implement a method of monitoring power coupled to said DUT"

(**Figure 7 item 702** discloses a processor coupled to a memory with instructions that when executed implement a method of monitoring power to the DUT **Col. 8 lines 5-37**), *"determining whether an activity signal from a DUT is received, said activity signal generated by said DUT when said DUT is coupled to an external power source"* (**Figure 4C, Col. 7 lines 32-43**), *"if said activity signal is received in generating a signal for preventing the coupling of power to said DUT from an in circuit emulator;"* (**Col. 8 lines 5-38**), *"if said activity signal is not received coupling power to said DUT from said in circuit emulator"* (**Figure 6 Col. 7 lines 60-67, Col. 8 lines 1-4**), *"if said activity signal is not received to decoupling power to said DUT from said in circuit emulator and generating a fault condition signal."* (**Col. 3 lines 29-33**).

4.2 As regards dependent **Claims 3 and 10** the *Coomes* reference teaches the functional equivalent of a pod connected to a DUT (**Figures 1a and 1b item 100**).

4.3 As regards dependent **Claims 4 and 11** the *Coomes* reference teaches more than one power source (**Figure 5** note items **V1, V2 and V3**).

4.4 As regards dependent **Claims 6 and 13** the *Coomes* reference discloses using a cable (**Figure 1A** note the 26 conductor cable).

4.5 As regards dependent **Claims 7, 14 and 20** the *Coomes* reference *inherently* teaches setting a bit in memory to indicate a fault condition (**Figure 7** note the item marked **RAM** and **Col. 8 lines 5-53**).

Allowable Subject Matter

5. **Claims 2, 5, 9, 12 and 16-19** are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5.1 As regards dependent **Claims 2, 9 and 16** the Examiner notes that the *Coomes* reference does not expressly disclose or provide proper motivation to teach the use of a power supply over-driver protection system in a system testing a micro controller.

5.2 As regards dependent **Claims 5, 12 and 18** the Examiner notes that the *Coomes* reference does not expressly disclose or provide the proper motivation to use a “clock signal” in a power supply over-driver protection system for a device under test.

5.3 As regards dependent **Claims 17** the *Coomes* reference does not expressly disclose or provide the proper motivation to make obvious the use of an FPGA in a power supply over-driver protection system.

5.4 As regards dependent **Claim 19** the *Coomes* reference does not expressly disclose or provide proper motivation to make obvious the use of a CAT5 cable, in fact this reference teaches away from using a CAT5 cable because a CAT5 cable has only eight conductors and the cable disclosed in **Figure 1A** is a 26 conductor cable.

Conclusion

6. **Claims 1, 3, 4, 6, 7, 8, 10, 11, 13, 14, 15 and 20** are rejected. **Claims 2, 5, 9, 12 and 16-19** are objected to. This Office Action is **Non-Final**.

6.1 The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

“A Production-oriented Measurement Method for Fast and Exhaustive Iddq Tests” by B. Laquai, H. Richter and H. Werkmann, IEEE 1997, discloses Iddq testing of a microcontroller by alternating the voltage and current from a power supply (**page 283**).

U.S. Patent 5,640,337 discloses methods of testing a DUT in a pod (**Figure 2c**).

6.2 Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dwin M Craig whose telephone number is (571) 272-3710. The examiner can normally be reached on 10:00 - 6:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Leo Picard can be reached on (571) 272-3749. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DMC



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